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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/517,464	07/14/2005	Mats Bladh	P15008US1	1355		
27045	7590	03/13/2008	EXAMINER			
ERICSSON INC. 6300 LEGACY DRIVE M/S EVR 1-C-11 PLANO, TX 75024				PERT, EVAN T		
ART UNIT		PAPER NUMBER				
2826						
MAIL DATE		DELIVERY MODE				
03/13/2008		PAPER				

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/517,464	BLADH, MATS	
	<b>Examiner</b>	<b>Art Unit</b>	
	EVAN PERT	2826	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 06 December 2004.  
 2a) This action is **FINAL**.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) 4-7 is/are allowed.  
 6) Claim(s) 1 and 2 is/are rejected.  
 7) Claim(s) 1-3 and 8-10 is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on 06 December 2004 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____ .                                    |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date <u>0107 and 1204</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application |
|  | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Claim Objections***

1. Claims 1-3 and 8-10 are objected to because of the following informalities:

In claim 1 and 8, the phrase “by the feedback arrangement varying frequency changes, phase and amplitude of the gate-to-source voltage” is objectionable since the feedback arrangement does not “vary frequency,” but rather varies “phase and amplitude” (of the gate-to-source voltage) “by varying frequency” (i.e. in accordance with variation in frequency) [see abstract of WO 03/03/105341, for example ].

Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 2 are rejected under 35 U.S.C. 102(b) as being anticipated by Bayruns (U.S. 5,047,728).

Regarding claim 1, the '728 reference discloses an active load arrangement (Z) (with “for providing output DC load to an object under AC test” being an intended use of which the '728 reference active load is capable), the arrangement including a voltage controlled transistor (M2) having a source (S), a gate (G) and a drain (D), wherein the drain (D) is associated with the gate (G) and [could be by “intended use”] connected to an arrangement input associated with the object and the source (S) is connected to an

arrangement output ( $V_{out}$ ) associated with the object, the active load arrangement further comprising a feedback arrangement (L2) connected to the source and to the gate for obtaining low impedance at low frequencies (i.e. L2 is a short-circuit at DC so M2 will be full on at DC) and high impedance at high frequencies by the feedback arrangement by varying phase and amplitude of the gate-to-source voltage as a function of frequency variation [i.e. the phase and amplitude of the gate to source voltage of M2 is a function of frequency, where L2 shifts phase 90 degrees and the frequency dependent impedance of L2 decreases amplitude of the gate voltage as frequency increases.

Regarding claim 2, the feedback arrangement includes an inductance (L2) connected between the source of M2 and the output ( $V_{out}$ ).

***Allowable Subject Matter***

3. Claims 4-7 are allowed.
4. Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
5. Claims 8-10 are objected to for informalities outlined under item 1 above, but are otherwise allowable.
6. The following is a statement of reasons for the indication of allowable subject matter:

Despite the indication of "x" references cited in the related international search report, the prior art does not disclose the claimed active load arrangement (e.g. for providing DC output load to an object under AC test), the arrangement particularly characterized by a MOSFET with first and second feedback networks, such as the inductance (L1) and resistance (R1) recited in claim 4, or active load arrangement with two active loads having shared feedback network, and connected in reverse to be able to share the same inductance [e.g. claims 8-10].

***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to EVAN PERT whose telephone number is (571)272-1969. The examiner can normally be reached on M-F (7:30AM-3:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sue Purvis can be reached on 571-272-1236. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

ETP  
March 3, 2008

/Evan Pert/  
Primary Examiner, Art Unit 2826